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LF 4000

QUAD LOGIC FAN IN/FAN OUT

FEATURES:

- * 4X4, 2X8, or 1X16 grouping
- * 16 inputs, 24 outputs
- * 100 MHz operation

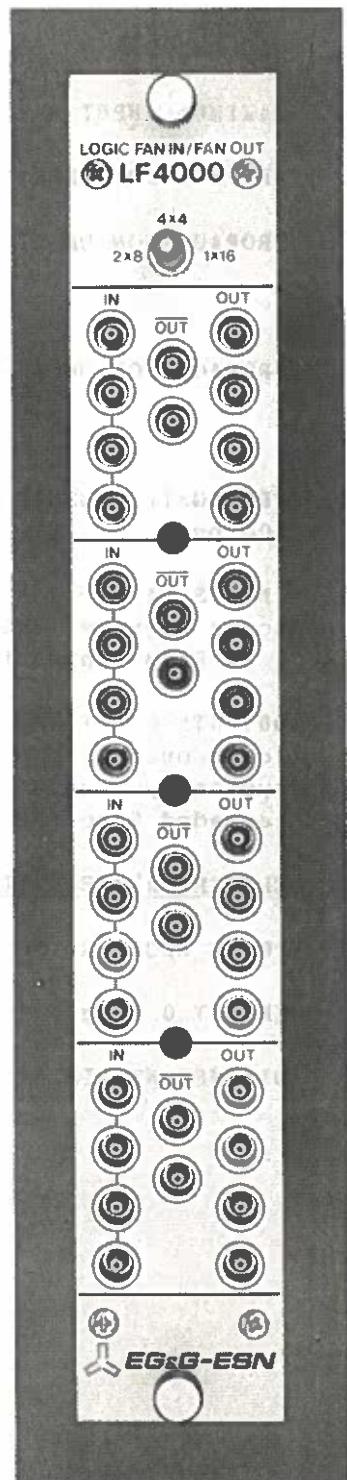
APPLICATIONS:

- * Coincidence experiments
- * Logic circuit interconnection
- * OR gate

The Model LF4000 Quad Logic Fan In / Fan Out provides a way to interconnect large numbers of logic modules with minimum additional delay time. The unit contains 16 negative Fast-NIM inputs that may be configured as a quad 4-input, 6-output OR gate; a dual 8-input, 12-output OR gate; or as a single 16-input, 24-output OR gate.

A front-panel toggle switch allows the selection of any of the three matrices, 4X4, 2X8, or 1X16. Front-panel LED's indicate when neighboring channels are connected. For every 4 non-inverting outputs there are 2 inverting outputs.

All inputs and outputs are negative Fast-NIM compatible. Care has been taken to minimize the propagation time variation from one channel to another.



SPECIFICATIONS:

NUMBER OF SECTIONS four; can be cascaded by means of front-panel toggle switch (4 X 4, 2 X 8, or 1 X 16 grouping possible).

MAXIMUM INPUT RATE 100 MHz.

PROPAGATION DELAY less than 8 ns.

PROPAGATION DELAY VARIATION BETWEEN INPUTS 4 X 4 : less than 200 ps
2 X 8 : less than 600 ps
1 X 16: less than 1.2 ns

PROPAGATION DELAY VARIATION BETWEEN OUTPUTS 4 X 4 : less than 500 ps
2 X 8 : less than 600 ps
1 X 16: less than 1.2 ns

PROPAGATION DELAY CHANGE DUE TO CHANGING 4 X 4 TO 2 X 8 OR 1 X 16 less than 100 ps.

INPUTS (4 per section) Front-panel LEMO connectors accept negative Fast-NIM logic signals. Minimum amplitude, -400 mV; minimum width, 4 ns FWHM; input impedance, 50 Ohms.

OUTPUTS (OUT and OUT) 4 non-inverting and 2 inverting negative Fast-NIM logic outputs per section. Nominal -16 mA output; risetime and falltime, 3 ns typically; output width is equal to logical OR of all inputs which are cascaded together.

ELECTRICAL/MECHANICAL

POWER REQUIRED -6 V, 980 mA

WEIGHT 0.5 kg

DIMENSIONS NIM single-width module, 3.3x22.1 cm front panel.

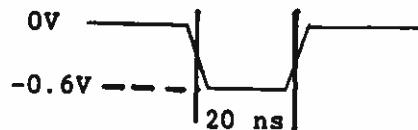


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1. Measure current on -6V power line.

Should be 800 mA 0-10%

2. INPUT SIGNALS

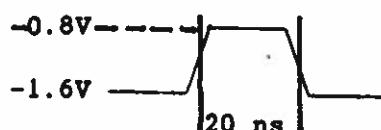


Rise and fall times should be approx. 2 ns. Pulse width 20 ns. Signal is negative -0.6V

Apply signal to NIM 1 input.

Set front panel switch to position "1 x 16"

Monitor testpoint TP 1, (square solder points on solder side of pc-board) ensure that scope probe is grounded, and check for following signal:-



Rise and fall times approx. 2 ns. Pulse width unchanged at 20 ns. Signal level -0.8V to -1.6V

Check for no signal on other outputs.

Continuing monitoring TP 1, apply input signal to the remaining three ch.1 inputs and verify the signal shown.

Repeat this procedure monitoring first TP 5, and then TP 9.

3. Repeat step 2 for channels 2 - 4 as follows:-

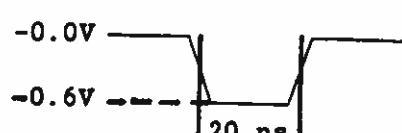
All inputs for ch 2 with test points TP 2,6 and 10

All inputs for ch 3 with test points TP 3,7 and 11

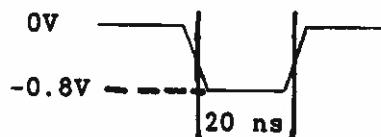
All inputs for ch 4 with test points TP 4,8 and 12

4. OUTPUT SIGNALS

Apply input signal as follows to any one of the ch 1 inputs of unit under test

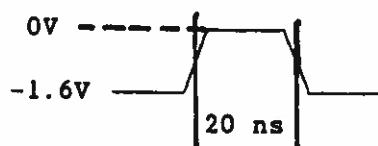


Monitor the first ch 1 "OUT" LEMO connector with an oscilloscope (terminate with 50 Ohm), the following signal should be seen:-



Verify that all four ch 1 "OUT" signals are the same.

Monitor "OUT" signals in same way and check for following signal:-



5. Repeat step 4 for the "OUT" and "OUT" outputs of ch 2, 3 and 4.

6. Front Panel Switch Operation

a) position "4 x 4"

None of the LED's should illuminate.

Apply input signal (see 1. above) to any input of ch 1.

Monitor the ch 1 "OUT" signal for correct waveform.

Check "OUT" outputs of ch 2, 3 and 4 for NO signal.

If OK, apply input signal to ch 2 and check that NO output signal is present on ch 1, 3 and 4.

Repeat procedure for ch 3 and 4.

b) position "2 x 8"

LED's between (ch 1/ch 2) and (ch 3/ch 4) should illuminate.

Apply input signal to ch 1 as above.

Check for output from "OUT" connectors of ch 1 and ch 2.
Check for NO output on ch 3 and ch 4.

Apply input signal to ch 2.

Check for output from "OUT" connectors of ch 1 and ch 2.
Check for NO output on ch 3 and ch 4.

Apply input signal to ch 3.

Check for output from "OUT" connectors of ch 3 and ch 4.
Check for NO output on ch 1 and ch 2.

Apply input signal to ch 4.

Check for output from "OUT" connectors of ch 3 and ch 4.
Check for NO output on ch 1 and ch 2.

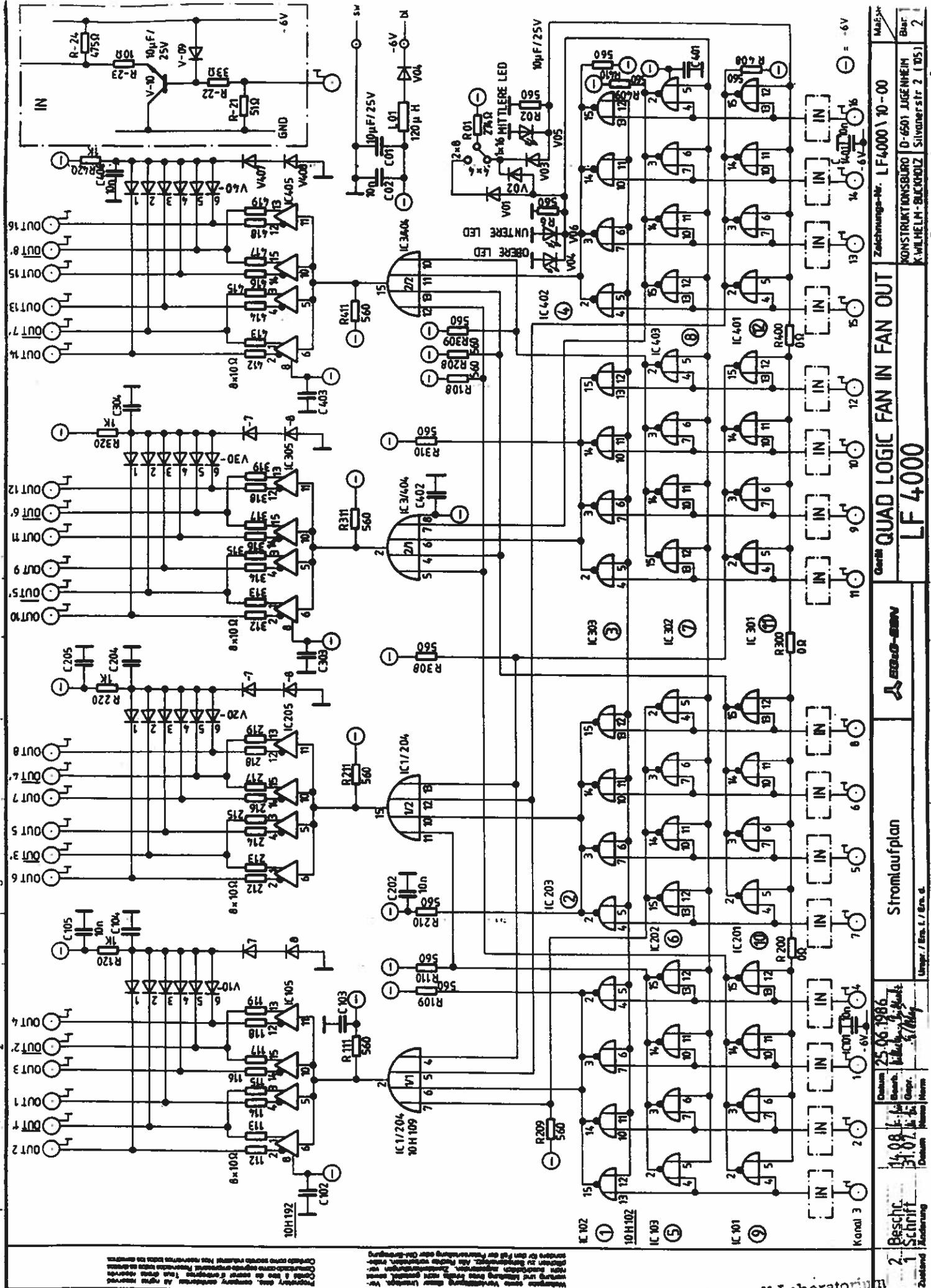
c) position "1 x 16"

All 3 LED's should illuminate.

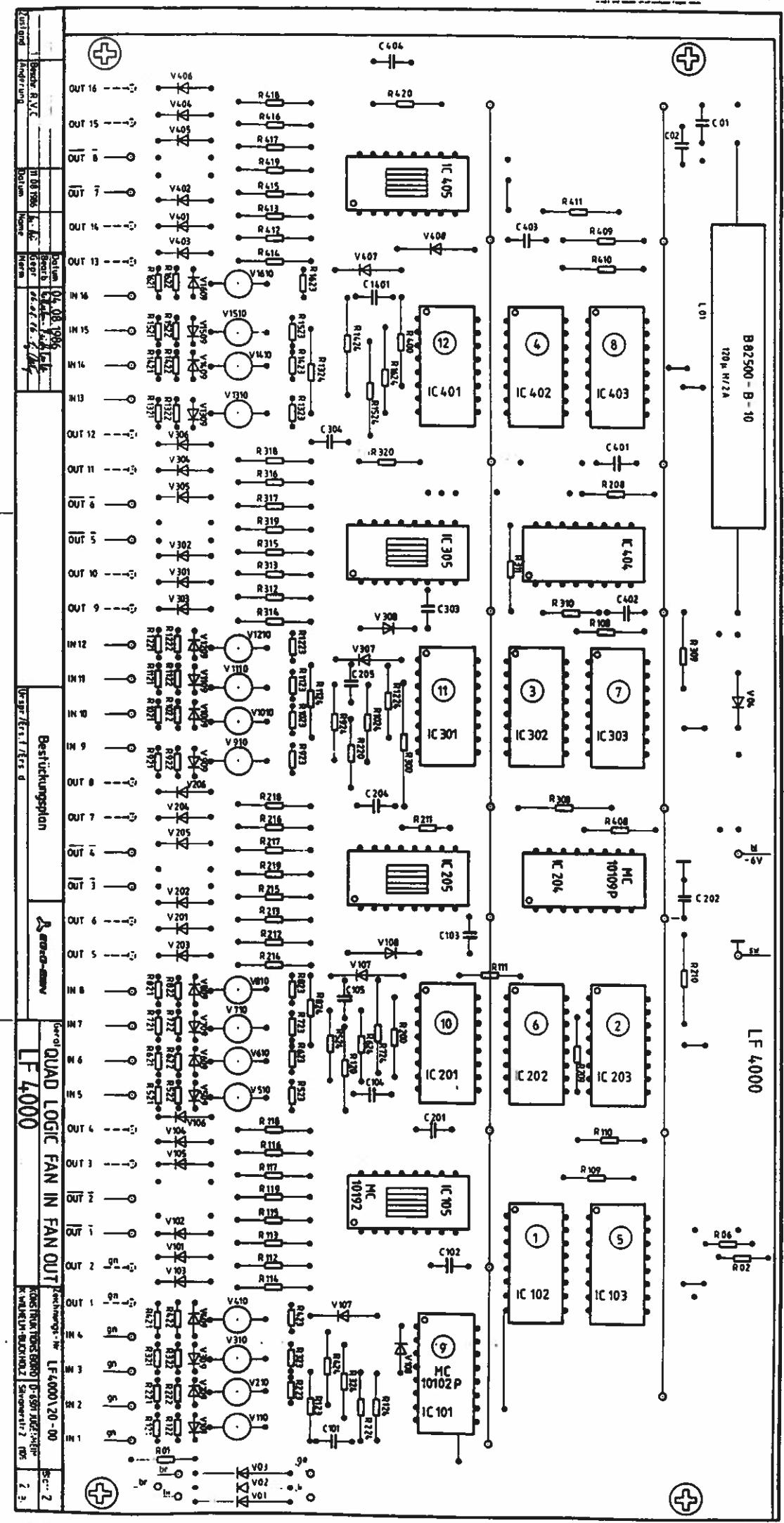
Apply input signal to ch 1.

Output signals should be present at all 4 channels.

Repeat, moving input signal successively to all four channels.



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P/N#	QTY	PART #	TYPE	VALUE	FORM	DESCRIPTION	UNIT PRICE	TOTAL PRICE	COMMENTS
1	33	10027	WIDERSTAND	10 OHM	RM 10	R112-119,212-219 312-319,412-419,123			
2	15	10257	WIDERSTAND	10 OHM	RM 2,5	R223,323,423,523,623 723,823,923,1023			RGU-M
3	16	10236	WIDERSTAND	33 OHM	RM 2,5	1123,1223,1323,1423 1523,1623			RGU-M
4	16	10234	WIDERSTAND	51 OHM	RM 2,5	R122,222,322,422,522 622,722,822,922,1022 1122,1222,1322,1422 1522,1622			RGU-M
5	1	10078	WIDERSTAND	270 OHM	RM 10	R121,221,321,421,521			
6	16	10090	WIDERSTAND	470 OHM	RM 10	621,721,821,921,1021 1121,1221,1321,1421 1521,1621			
7	1	10094	WIDERSTAND	560 OHM	RM 10	R124,224,324,424,524			
8	17	10096	WIDERSTAND	560 OHM	RM 10	624,724,824,924,1024 1124,1224,1324,1424 1524,1624			
9	4	10110	WIDERSTAND	1 K	RM 10	R209	R02,06,108-111,208 210,211,308-311		
10	3	10227	WIDERSTAND	SERO OHM	RM 10	R120,220,320,420	408-411		
11	17	12141	KONDENS.-VIELS.	10nF	RM 2,5	R200,300,400	C101-105,201,202,204		
12	1	12184	KONDENS.-TANTAL	10uF/25V	RM 5	205,303,304,401-404 1401,02			
13	1	13004	DROSSEL	120uH	CO1				
14	1	14039	DIODE	1N 4007	L01				
15	16	14042	DIODE	IN 4151	V04				
						V109,209,309,409,509 609,709,809,909,1009 1109,1209,1309,1409 1509,1609			

1	EGG-ESN	MODULE:	LF 4000	PAGE:
2		DESCRIPTION:	QUAD LOGIC FAN IN/FAN OUT	1 of 2
3	100187	COMMENTS:		
4	None			