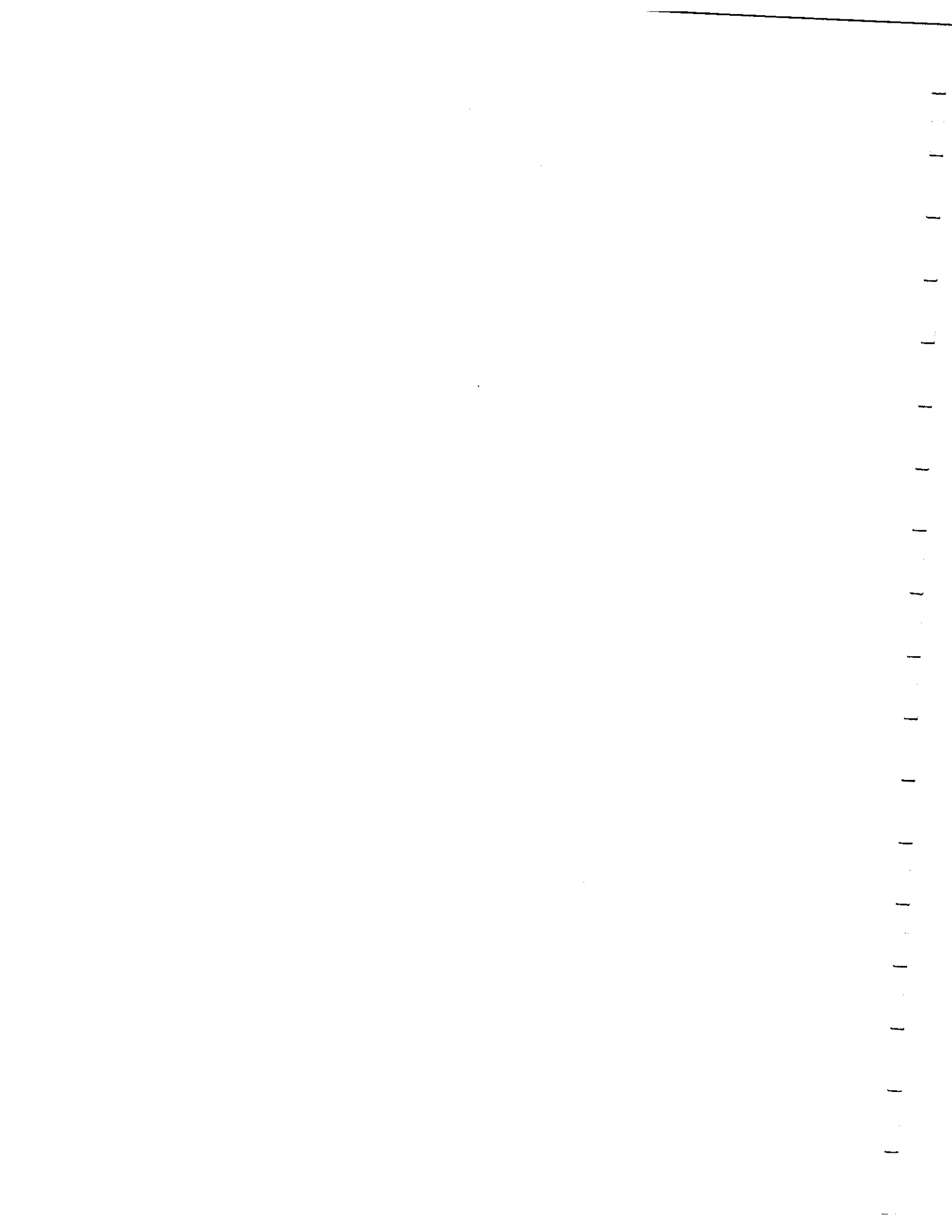


**Multichannel Analyzer  
Model Multiport II**

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11/03

**User's Manual Addendum**  
CSERF DN3024296



## **Addendum to the Multiport II Manual**

### Mossbauer Output CSERF - DN3024296

The FPGA on the MCA Card has been modified to bring the 2048 bit out on the Rear Panel I/O connector's DT signal line. Mossbauer measurements have to be done with the MCS Range set to 4K.

The DT signal BNC on the C1801 cable will be a TTL Low during the first half of MCS collect, and a TTL High during the second half of MCS Collect.

If the MCS Input is set to LLD/ULD, the standard C1801 cable supplied with the MCA card is all that is needed. If the MCS Input is set to TTL, then the optional C1804 cable, which has the TTL Input BNC, must be used instead of the C1801 Cable.

