



# Model 2004 Semiconductor Detector Preamplifier

## Features

- Low noise design: less than 2.8 keV (Si) at 0 pF
- High charge rate capability: up to  $4.5 \times 10^6$  MeV/sec
- FET input, diode protected
- Pole/zero select or defeat
- High voltage operation up to  $\pm 2000$  V dc

## Description

The CANBERRA Model 2004 charge sensitive FET input preamplifier was designed for use with both low and high capacitance semiconductor detectors. The preamp converts the charge carriers developed in the detector during each absorbed nuclear event to a step function voltage pulse, the amplitude of which is proportional to the total charge accumulated in that event. The output provides a positive polarity signal when used with detectors requiring positive bias and decays with a nominal time constant of 50  $\mu$ s.

The high charge rate capability of the design is evidenced by a charge rate capacity of better than  $2 \times 10^{-7}$  coulombs per second, or  $4.5 \times 10^6$  MeV per second for silicon detectors. In order to take full advantage of such a high count rate capability, a main amplifier with a correspondingly high count rate ability, such as the CANBERRA Model 2025 or Model 2026, should be employed.

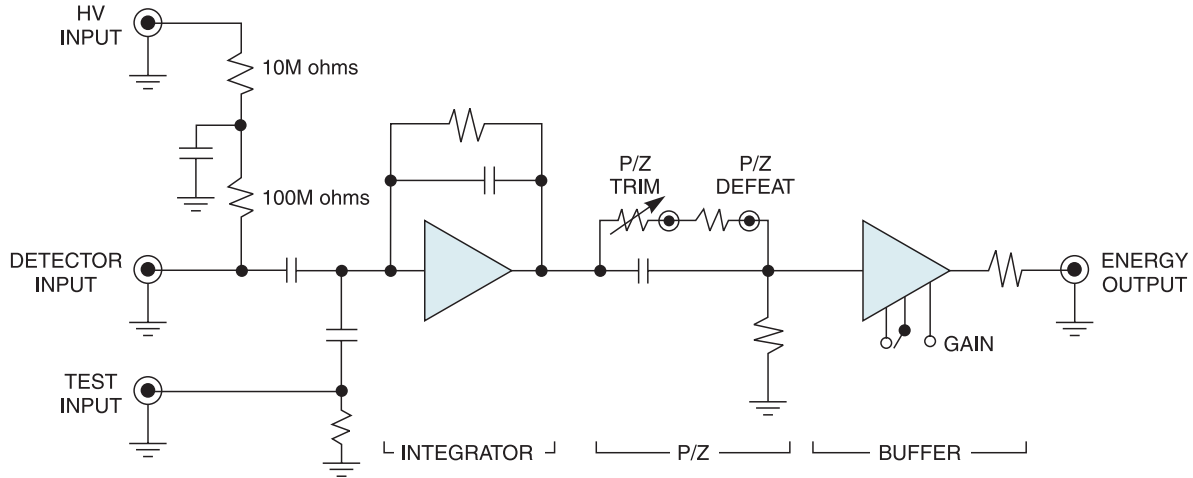
As shown in the functional schematic, the first stage acts as an operational integrator which produces an output potential proportional to the accumulated charge. This integrator is coupled to an output buffer stage through a pole/zero trim network. A feature of particular importance to certain users is a removable socketed resistor on the printed circuit board for disabling the pole/zero circuit. For low count rate applications using high leakage detectors, the resulting low frequency noise bandwidth may improve resolution.

The preamplifier offers a noise contribution of only 2.8 keV, FWHM, Si, with a rate of noise increase with increasing input capacitance of only 10 eV per picofarad, FWHM, Si. Conversion gains of nominally 9 mV per MeV or 45 mV per MeV (Si) may be selected by a jumper plug on the printed circuit board inside the unit.

In addition, the input circuit includes a protection network to prevent damage to the input FET from transient high voltage waveforms caused by sudden application or removal of detector bias, or a detector fault. All necessary power is provided by a CANBERRA main amplifier through the 300 cm (10 ft) compatible cable.



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Functional Schematic

## Specifications

### INPUT

- DETECTOR INPUT – Accepts charge pulse from semiconductor detector.
- TEST INPUT – Charge coupled to preamp input at 2.2 pC/V;  $Z_{in} = 93 \Omega$ .
- HV INPUT – Allows detector biasing up to  $\pm 2000$  V dc, detector series bias resistance 110 M $\Omega$ .

### OUTPUTS

- ENERGY OUTPUT – Inverted tail pulse, rise time as in Table 1, 50  $\mu$ s fall time constant, up to  $\pm 10$  V;  $Z_{out} = 93 \Omega$ , direct coupled.

### PERFORMANCE

- INTEGRAL NONLINEARITY –  $< \pm 0.02\%$  for  $\pm 10$  V output.
- GAIN DRIFT –  $< \pm 0.01\%$  per  $^{\circ}\text{C}$  ( $\pm 100$  ppm/ $^{\circ}\text{C}$ ).
- DETECTOR BIAS ISOLATION –  $\pm 5000$  V dc maximum.
- NOISE OUTPUT PERFORMANCE – See Table 1.
- CHARGE SENSITIVITY – 0.2 V/pC or 1.0 V/pC, internally selected.
- ENERGY SENSITIVITY (Si) – 9 mV/MeV or 45 mV/MeV, internally selected.
- RISE TIME – See Table 1.

### CONNECTORS

- POWER – Amphenol 17-20090.
- DETECTOR INPUT – SHV
- HV INPUT – SHV
- TEST INPUT and ENERGY OUTPUT – BNC
- 300 cm (10 ft) preamp power cable is supplied with the preamplifier.

### POWER REQUIREMENTS

- +24 V dc – 30 mA
- +12 V dc – 2 mA
- 24 V dc – 15 mA
- 12 V dc – 2 mA

### PHYSICAL

- SIZE – 9.86 x 7.62 x 4.45 cm (3.88 x 3.00 x 1.75 in.).
- NET WEIGHT – 0.23 kg (0.5 lb).
- SHIPPING WEIGHT – 1.0 kg (2.2 lb).

### ENVIRONMENTAL

- OPERATING TEMPERATURE – 0 to 50  $^{\circ}\text{C}$ .
- OPERATING HUMIDITY – 0 to 80% relative, non-condensing.
- Meets the environmental conditions specified by EN 61010, Installation Category I, Pollution Degree 2.

Table 1

$C_{source}$ in pF	Noise in Coulombs rms	Noise in keV, FWHM, Si	Rise Time in Nanoseconds
0	$< 5.3 \times 10^{-17}$	$< 2.8$	$< 20$
30	$< 5.7 \times 10^{-17}$	$< 3.0$	$< 21$
100	$< 6.6 \times 10^{-17}$	$< 3.5$	$< 23$
300	$< 1.0 \times 10^{-16}$	$< 5.3$	$< 33$

Noise performance using CANBERRA Model 2025 Spectroscopy Amplifier with 2  $\mu$ s, unipolar, near-Gaussian shaping.



